

# United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.			
09/768,073	01/23/2001	William Frederick Sauber	16356.573 (DC-02636)	2498			
27683	7590 07/12/2004		EXAMINER				
HAYNES AND BOONE, LLP 901 MAIN STREET, SUITE 3100			NGUYEN	NGUYEN, HAU H			
DALLAS, T			ART UNIT	PAPER NUMBER			
•			2676	13			
			DATE MAILED: 07/12/2004	DATE MAILED: 07/12/2004			

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	on No.	Applicant(s)				
		09/768,07		SAUBER, WILLIAM FREDERICK				
4 .	Office Action Summary	Examiner	J	Art Unit				
	•	Hau H Ng	LIVOD	2676	•			
	The MAILING DATE of this communication				ess			
Period for Reply								
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE <u>03</u> MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).  Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).								
Status								
1)⊠	Responsive to communication(s) filed on 05	5 May 0204.						
2a)⊠	☐ This action is <b>FINAL</b> . 2b)☐ This action is non-final.							
3)□	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is							
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.							
Dispositi	on of Claims							
4)⊠	∑ Claim(s) <u>1-21</u> is/are pending in the application.							
-	4a) Of the above claim(s) <u>2-5,7-8,11-14,16-17,19-20</u> is/are withdrawn from consideration.							
5)□	Claim(s) is/are allowed.							
6)⊠	☐ Claim(s) <u>1,6,9,10,15,18,21</u> is/are rejected.							
7)	Claim(s) is/are objected to.							
8)□	B) Claim(s) are subject to restriction and/or election requirement.							
Applicati	on Papers							
9)□	The specification is objected to by the Exam	iner.						
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.								
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).							
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)	The oath or declaration is objected to by the	Examiner. No	te the attached Office	Action or form PTO-	152.			
Priority u	ınder 35 U.S.C. § 119							
12)	Acknowledgment is made of a claim for forei  ☐ All b) ☐ Some * c) ☐ None of:	ign priority und	ler 35 U.S.C. § 119(a)	-(d) or (f).				
۵٫۱	1. ☐ Certified copies of the priority docume	ents have beer	n received.					
	2. Certified copies of the priority docume			on No				
	3. Copies of the certified copies of the priority documents have been received in this National Stage							
	application from the International Bure							
* S	see the attached detailed Office action for a l	ist of the certif	ied copies not receive	d.				
Attachmen	t(s)							
1) 🔲 Notic	e of References Cited (PTO-892)		4) Interview Summary					
	e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/	08)	Paper No(s)/Mail Da 5) Notice of Informal Pa		2)			
	r No(s)/Mail Date	00)	6) Other:		•			

Art Unit: 2676

## Response to Amendment

1. Applicant's arguments with respect to claims 1, 6, 9, 10, 15, 18, and 21 have been considered but are most in view of the new grounds of rejection.

## Claim Rejections - 35 USC § 112

- 2. The following is a quotation of the second paragraph of 35 U.S.C. 112:
  - The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.
- 3. Claim 15 recites the limitation "the first display device", "the first signal", "the second signal". There is insufficient antecedent basis for this limitation in the claim.

#### Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 1, 6, 9, 10, 15, 18, and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over admitted prior art in view of Zenda (U.S. Patent No. 5,559,525).

Referring to claims 1, 6, 9, 10, 18, and 21, as shown in Fig. 1 and described on page 1, lines 20-23, and page 2, lines 1-2 of the specification, admitted prior art teach a processor 110 is coupled to a chipset 120 that includes a bus input/output (I/O) controller 122, a memory controller 124, and an integrated video controller 126. A system memory 130 is coupled to

Art Unit: 2676

chipset 120. An optional video controller 140 and a memory 142 are also included in computer system 100. Video controller 140 is coupled to chipset 120 using a port 144 such as an AGP port.

Thus, admitted prior art teach all the limitation of claims 1, 10, and 21, except for a switching device configured to receive analog and digital signals from the video controllers and provide the signals to a compatible display device.

However, Zenda teach a portable computer having a first graphics subsystem for outputting data to be displayed and a flat panel display unit means for displaying the data supplied from the first graphics subsystem; a second graphics subsystem to be externally connected to the portable computer, for supplying data to be displayed on the flat panel display unit means, to the portable computer; connectors for connecting the second graphics subsystem to the portable computer (col. 2, lines 64-67, and col. 3, lines 1-8). With reference to Fig. 3A, Zenda teaches the first display controller 87 and the RAMDAC 93 constitute a first graphics subsystem (built-in graphics subsystem) (chipset) (see col. 8, lines 12-14). As shown in Fig. 3A, an LCD display board comprising a second display controller 109 is connected to the chipset. The system further comprises a first display device 91, and a second display device 107. Zenda further teaches the selector 113 (a switching device) is connected to the first display controller 87 by the feature connector 103, and to the color LCD controller 95 by the Z connector 105. As illustrated in FIG. 7, the selector 113, in response to the changeover signal 115 supplied from the second display controller 109, selects (1) either the digital color data supplied from the RAMDAC 111 or the digital color data supplied from the second display controller 109, (2) either the analog video signals supplied from the RAMDAC 111 or the analog video signals

Art Unit: 2676

from the second display controller 109, and (3) either the control signals coming from the first display controller 87 or the control signals transmitted from the second display controller 109. The selector 113 outputs the selected digital color data and analog video signals (col. 9, lines 27-40) (please see also column 13, lines 15-21). Thus, selector 113 receives both analog and digital signals and provides to a compatible display device.

Therefore, it would have been obvious to one skilled in the art to utilize the switching device as taught by Zenda in combination with admitted prior art in order to selectively display, on a flat panel display unit, image data from a standard graphics subsystem or an optional graphics subsystem (col. 2, lines 47-50).

In regard to claim 15, as applied to claim 10, admitted prior art teach all the limitations of claim 15, except for a program causing the switching device to provide a first signal from the first video controller to the display device, and causing the switching device to provide the second signal from the second video controller to the display device in response to the second video controller being coupled to the interface.

However, Zenda teach when second graphics subsystem is not connected to the portable computer, the color LCD controller 95 displays the image defined from the first graphics subsystem on the color LCD panel 91 (col. 12, lines 17-36). Zenda further teach when the second graphics subsystem is connected to the computer system, in case where the changeover signal 115 indicates selection of the signals from the second display controller 109, the selector 113 selects the digital color data, the analog video signals and the control signals from the second display controller 109 (col. 13, lines 13-30).

Art Unit: 2676

#### Conclusion

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hau H. Nguyen whose telephone number is: 703-305-4104. The examiner can normally be reached on MON-FRI from 8:30-5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Matthew Bella can be reached on 703-308-6829.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D. C. 20231

or faxed to:

Art Unit: 2676

(703) 872-9314 (for Technology Center 2600 only)

Hand-delivered response should be brought to Crystal Park II, 2121 Crystal Drive, Arlington, VA, Sixth floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Technology Center 2600 Customer Service Office whose telephone number is (703) 306-0377.

H. Nguyen

07/08/2004

ULKA J. CHAUHAN

REIMARY EXAMINER